APPLICATION DATA SHEET

Secrecy Order in Parent Appl.?::

Application Information

Application number:: Filing Date:: Regular Application Type:: Utility Subject Matter:: Suggested classification:: Suggested Group Art Unit:: None CD-ROM or CD-R?:: Number of CD disks:: Number of copies of CDs:: Sequence submission?:: Computer Readable Form (CRF)?:: No Number of copies of CRF:: METHOD AND APPARATUS OF RELOADING Title :: **ERRONEOUS CONFIGURATION DATA** FRAMES DURING CONFIGURATION OF PROGRAMMABLE LOGIC DEVICES 852463.403 Attorney Docket Number:: No Request for Early Publication?:: No Request for Non-Publication?:: Suggested Drawing Figure:: 5 Total Drawing Sheets:: No Small Entity?:: No Petition included?:: Petition Type:: Licensed U.S. Gov't Agency:: Contract or Grant No::

No

First Applicant Information

Applicant Authority Type:: Inventor

Primary Citizenship Country:: India

Status:: Full Capacity

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Family Name::

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Country of Residence:: India

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Applicant Authority Type:: Inventor

Primary Citizenship Country:: India

Status:: Full Capacity

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Third Applicant Information

Applicant Authority Type:: Inventor

Primary Citizenship Country:: India

Status:: Full Capacity

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Country of mailing address:: India

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Correspondence Information

Correspondence Customer Number :: 00500

R presentative Information

Representative Customer Number::	00500
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Domestic Priority Information

Application ::	Continuity Type::	Parent Application::	Parent Filing Date::

Foreign Priority Information

Country::	Application number::	Filing Date::	Priority Claimed::
India	958/Del/2002	09/20/02	Yes

Assignee Information

Assignee name::	STMicroelectronics Pvt. Ltd.
Street of mailing address::	Plot No. 2 & 3, Sector 16A, Institutional Area, Noida
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State or Province of mailing address::	
Country of mailing address::	India
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